

# Inviting Designers, who believe



## "Small is Beautiful"

If the prospect of a challenging career with a global major excites you, please e-mail your résumé, to:

[hr-bangalore@infineon.com](mailto:hr-bangalore@infineon.com)

Please mention contact number, years of experience, skill set and domain expertise, in the subject line.

We are currently NOT looking for MCAs and freshers.

**Human Resources  
Department,**

**Infineon Technologies India  
Pvt. Ltd.**

10th Floor, Discoverer,  
International Tech Park,  
Whitefield Road,  
Bangalore - 560 066. India.

In the most complex domain of Semiconductor solutions - we have established global leadership. And we owe our success to our people - fine talents, fiercely focused on defining the future in technologies of tomorrow. If you are one of those rare professionals, who had the courage and passion to build invaluable experience in electronics design / library development, you can translate your expertise into global competence and unbridled growth.

**Infineon Technologies AG**, Munich, Germany - one of the top six Semiconductor companies in the world - offers semiconductor and systems solutions for applications in the wired & wireless communications markets; security systems and smart cards; automotive and industrial sectors; as well as memory products. With a strong global presence - operating from San Jose (CA) in US, Singapore in Asia Pacific, and Tokyo in Japan - Infineon reaches out to 135 countries with 1800 products, relying on the strength of 30000+ professionals worldwide. In the fiscal year ending September 2002, the company achieved sales of Euro 5.2 billion. Infineon is listed on the DAX index of the Frankfurt Stock Exchange and the New York Stock Exchange (Ticker symbol: IFX).

For our **Design Automation Technology Group** at India Development Center, Bangalore... we are looking for engineering professionals with 3-12 years experience at various levels.

### **LIBRARY Development**

To develop world-class, leading-edge design libraries for the latest CMOS process technologies with a focus on development on 90nm and pre-development activities for 65nm.

**Skills:** • Experience in CMOS library development (standard cells, data path cells, SRAM/ROM, IOs, basic analog functions such as PLL), primarily in 130nm or below • Standard Cells, Datapath: library architecture definition; full custom library circuit and layout development; deep submicron electrical characterization

(timing, power, leakage) • SRAM/ROM development; characterization and analysis w.r.t. DSM effects (IR drop, X-Talk, etc.) • IO development and characterization with familiarity on standard and advanced interfaces (CMOS, HSTL, LVTTTL, PCI, USB, LVDS, DDR, RapidIO, etc.); Knowledge of ESD protection concepts; IO production testing • View modeling for advanced EDA tools (DC, Apollo/Astro, Magma etc.); Regression testing concepts for EDA views • Silicon verification of advanced design libraries: testchip architecture and design; test program development; analysis of testing data • IC product development experience using CMOS design libraries (highly desirable). Preference will be given to those who have experience in electrical characterization of design libraries (including SW development for this task).

### **Design Flow**

To design and develop high performance semiconductors, the team will closely work with the Design team and add a level of detail to the success of creating state-of-the-art memory products.

### **Software Engineers**

• C++ • UML methodology or equivalent (Object Oriented design methodology) • Knowledge of Clearcase/EDA software, a plus

### **EDA tools Application Engineers**

• EDA tools from the user point of view • Good knowledge of PERL/TCL scripting languages • Knowledge of C++ and Object Oriented methodology, a plus • Mindset oriented on automation, regression testing

### **I/C Design Engineers (Analog and Digital)**

• IC design • Knowledge of flow issues and Design Methodology in the 90 and 65 nanometer space • PERL/TCL scripting languages and expertise in using a couple of EDA tools, a plus

[www.infineon.com](http://www.infineon.com)



Never stop thinking.